IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

NAVEED MAJID ET AL .

PHA 23,843

Serial No.

Filed: CONCURRENTLY

MULTIPLE SEMICONDUCTOR CHIP (MULTI-CHIP) MODULE FOR USE IN HIGH-POWER APPLICATIONS

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

#2/IBS

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Sir:

Enclosed is a Form PTO-1449 and copies of documents listed thereon. These documents are considered to be relevant in that they have been:

Х	considered in drafting the specification of the above- referenced application;
Х	cited in the specification of the above-referenced application; or
	cited as an "X" or "Y" document in a foreign Patent Office search report on a foreign counterpart application a copy of which report is also enclosed. I hereby certify that these documents were cited in said search report not more than three (3) months ago.
	Please charge any fee under 1.17(p) for this Information Disclosure Statement to be considered, not exceeding \$240.00, to Account No. 14-1270.

If readily available, English-language counterparts have been substituted for foreign-language patent documents. This disclosure is not an admission that any of these documents is material to or even prior art with respect to the above-referenced application.

Respectfully submitted,

Steven R. Biren, Reg. 26,531

Attorney

(914) 333-9630